

FIG. 1(RELATED ART)

LL	HL	HL
LH	HH	
LH	HH	
LH	HH	

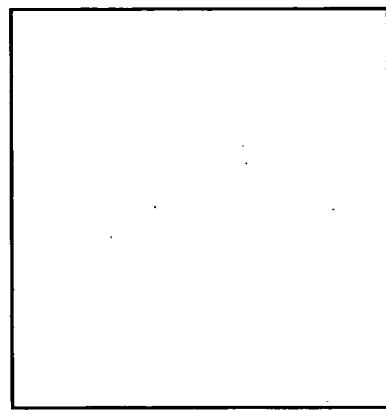


FIG. 2A  
(RELATED ART)

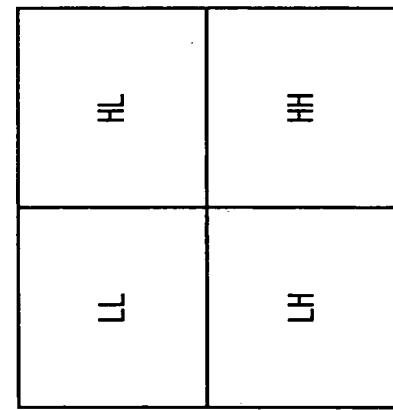
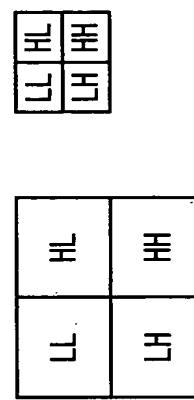


FIG. 2B  
(RELATED ART)

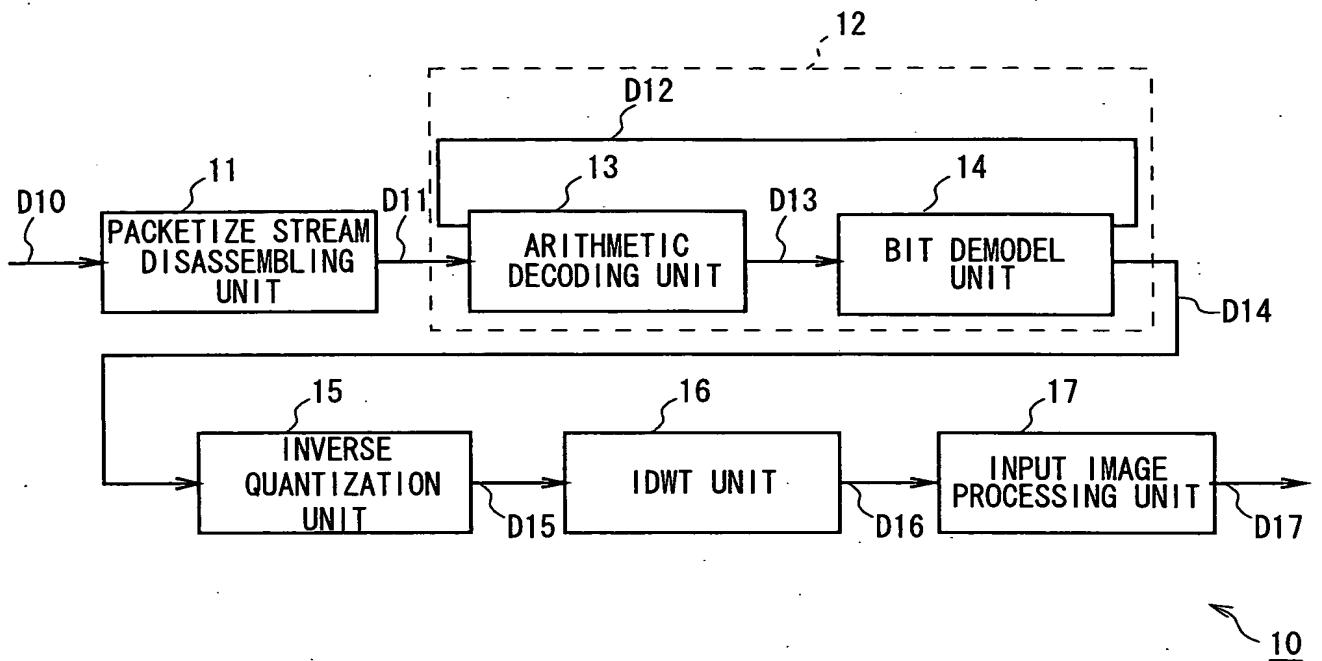


FIG. 3(RELATED ART)

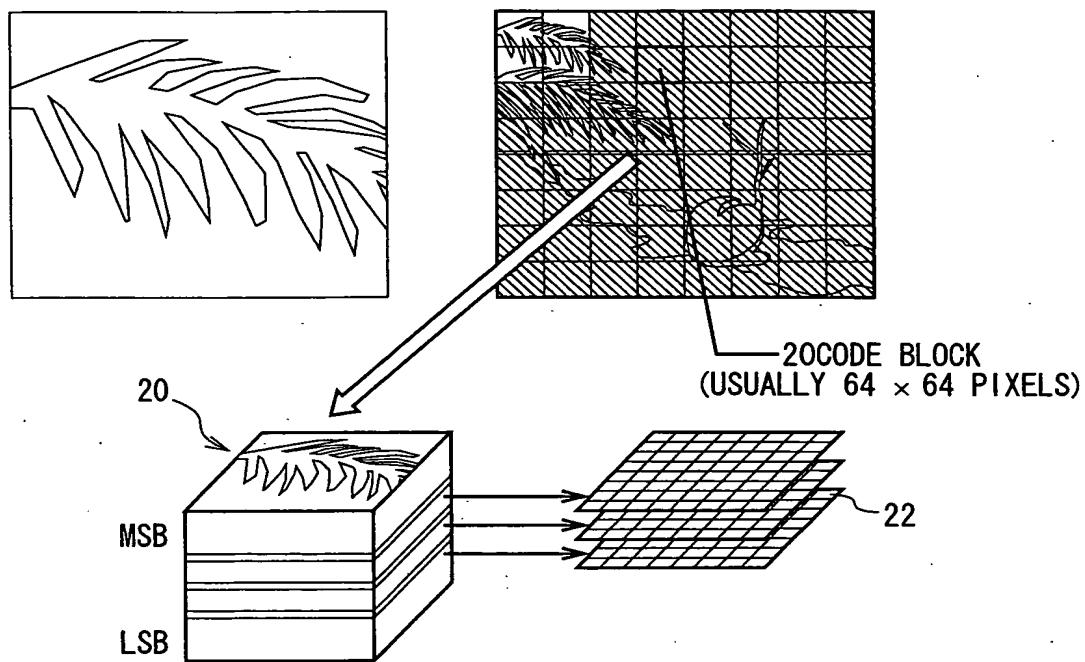
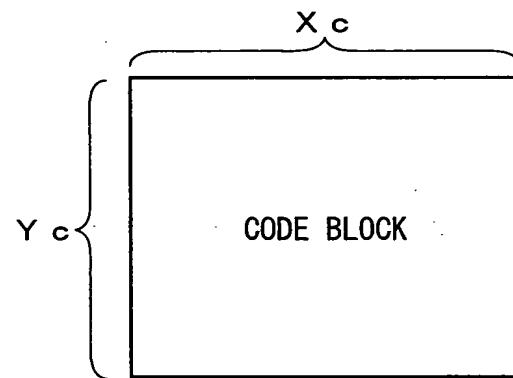


FIG. 4



X<sub>c</sub> AND Y<sub>c</sub> ARE POWERS OF 2  
 •  $4 \leq X_c \leq 1024$ ,  $4 \leq Y_c \leq 1024$   
 •  $X_c \times Y_c \leq 4096$

FIG. 5

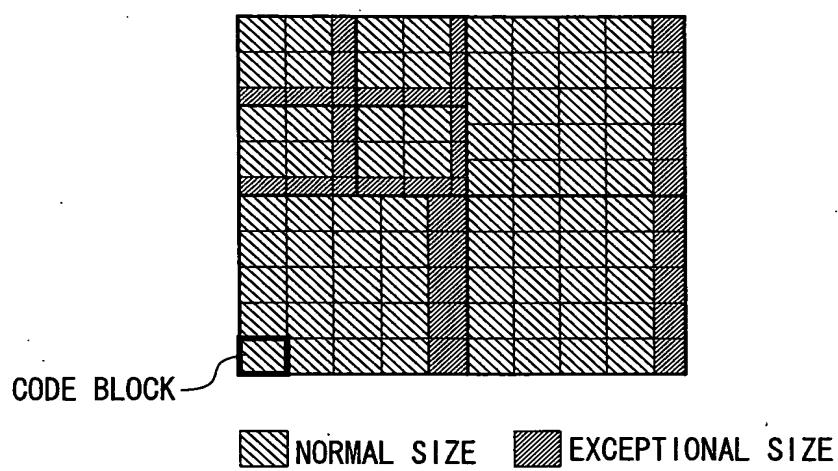


FIG. 6

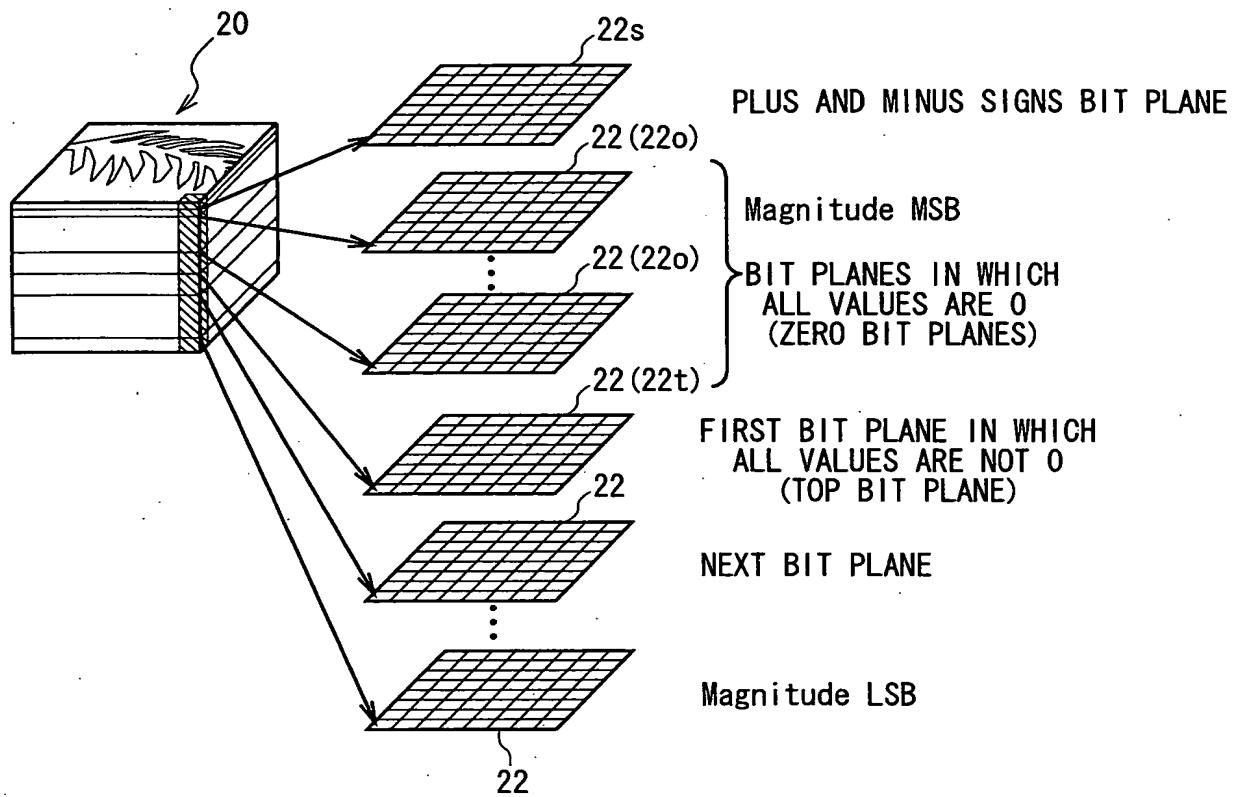


FIG. 7

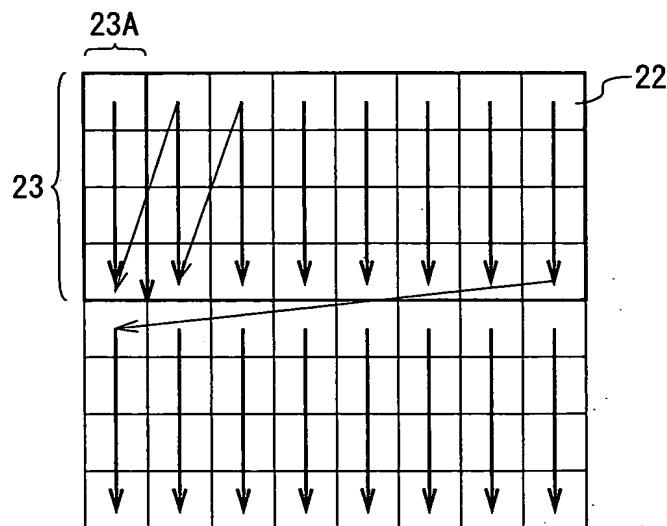
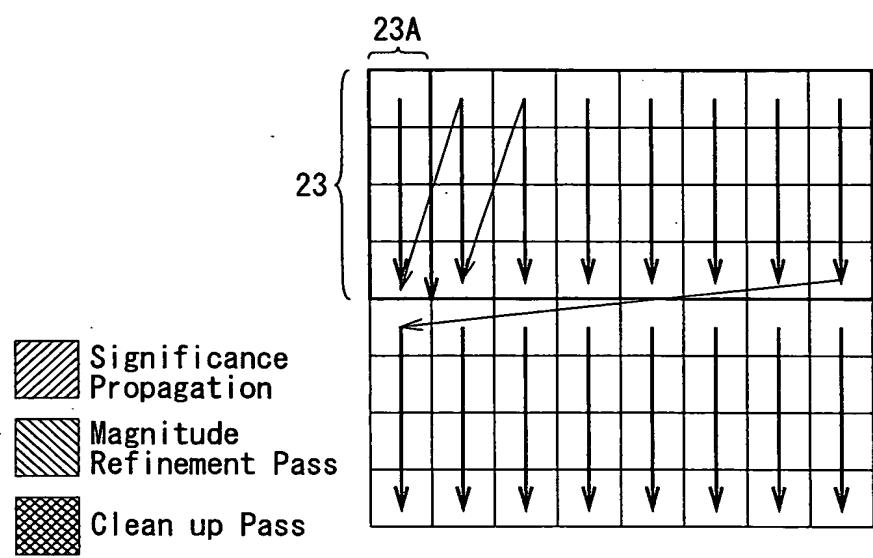
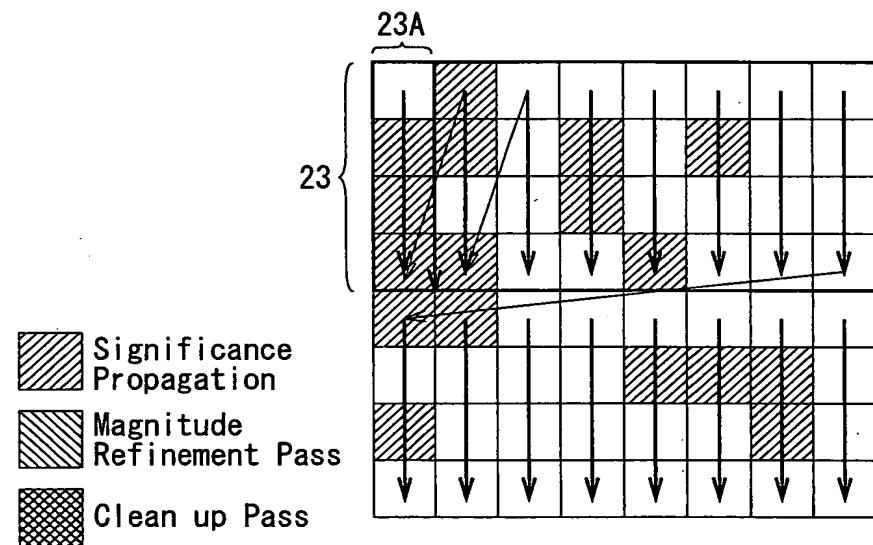


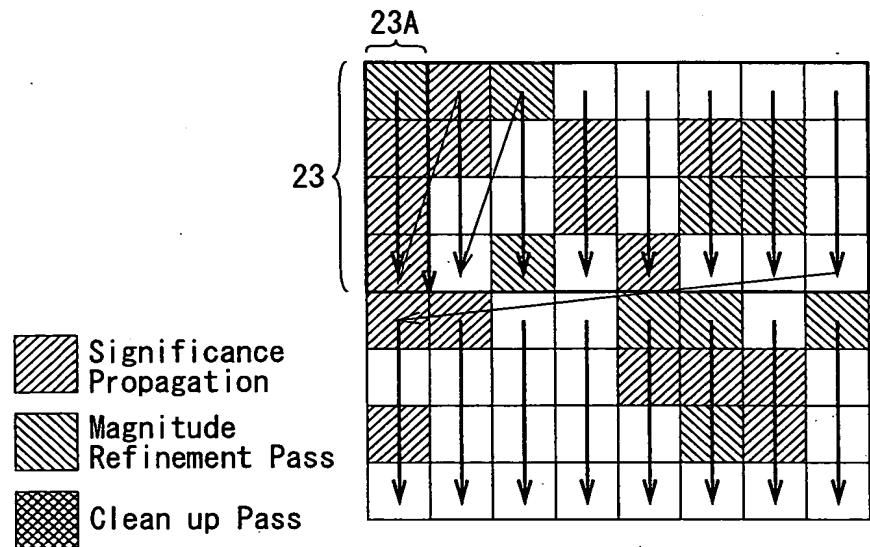
FIG. 8



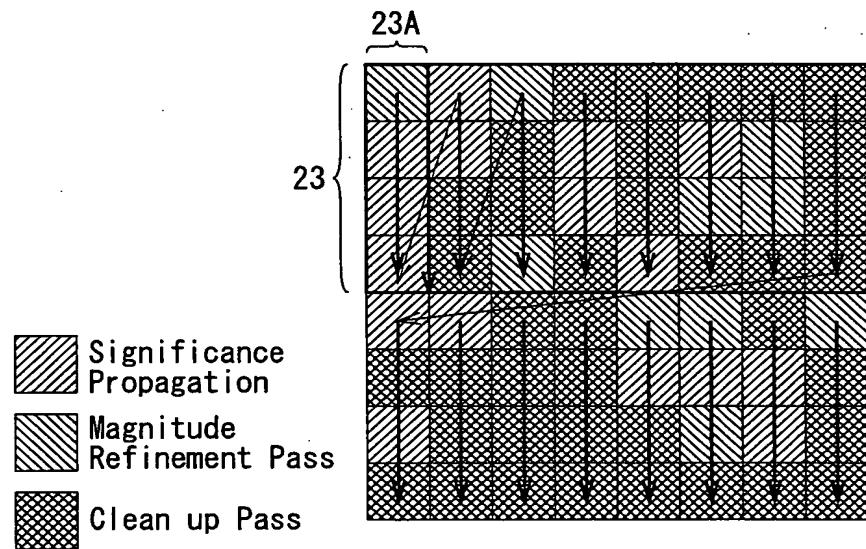
**FIG. 9**



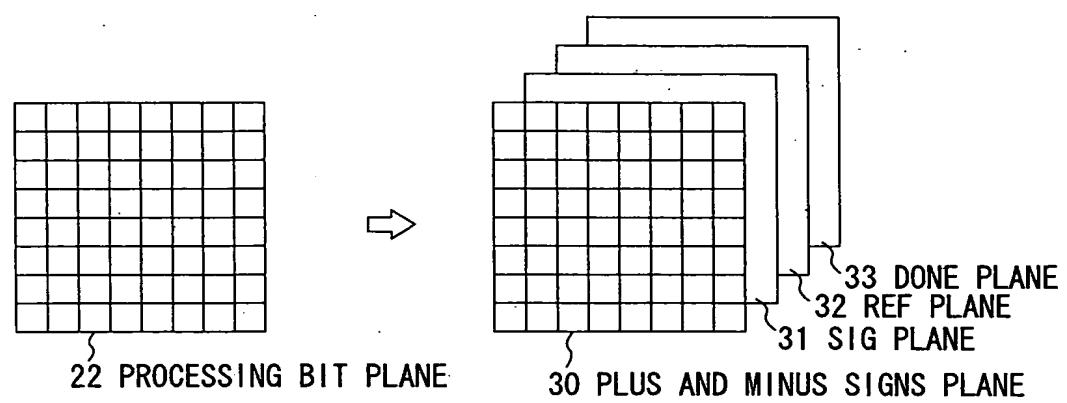
**FIG. 10**



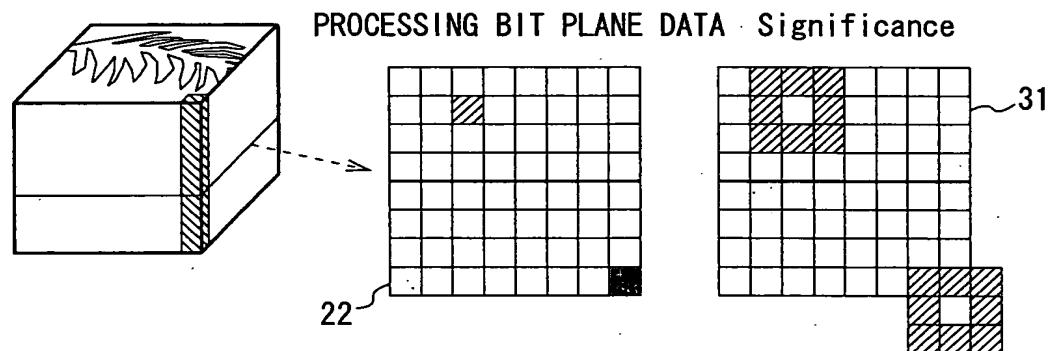
**FIG. 11**



**FIG. 12**



**FIG. 13**

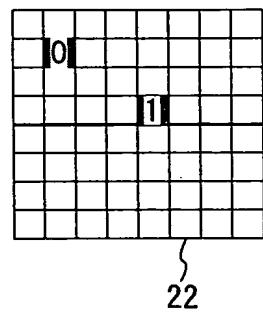


**FIG. 14A**

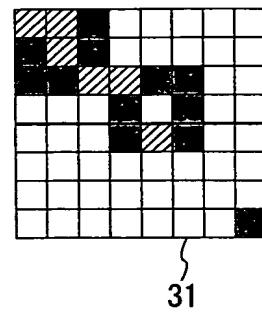
**FIG. 14B**

**FIG. 14C**

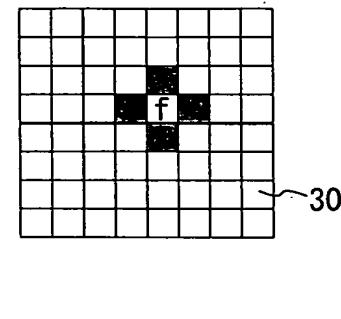
PROCESSING BIT PLANE



SIG PLANE



PLUS AND MINUS SIGNS PLANE



Significance

FIG. 15A

FIG. 15B

FIG. 15C

CONDITIONS	Context	Symbol
DATA, WHICH IS INSIGNIFICANT ITSELF AND IN THE VICINITY OF WHICH SIGNIFICANT EXISTS, IS 0	Cx	0
DATA, WHICH IS INSIGNIFICANT ITSELF AND IN THE VICINITY OF WHICH SIGNIFICANT EXISTS, IS 1 CHANGE TO SIGNIFICANT WITH DATA OF 1, AND SUBSEQUENTLY SIGN IS CHANGED TO BIT MODEL	Cx	1
	Sign cx	f

FIG. 15D

LL OR LH COMPONENT			HL COMPONENT			HH COMPONENT		CX	BIT
$\Sigma H$	$\Sigma V$	$\Sigma D$	$\Sigma H$	$\Sigma V$	$\Sigma D$	$\Sigma H + \Sigma V$	$\Sigma D$		
2	-	-	-	2	-	-	$\geq 3$	8	X
1	$\geq 1$	1	$\geq 1$	1	-	$\geq 1$	2	7	
1	0	$\geq 1$	0	1	$\geq 1$	0	2	6	
1	0	0	0	1	0	$\geq 2$	1	5	
0	2	-	2	0	-	1	1	4	
0	1	-	1	0	-	0	1	3	
0	0	$\geq 2$	0	0	$\geq 2$	$\geq 2$	0	2	
0	0	1	0	0	1	1	0	1	
0	0	0	0	0	0	0	0	0	

FIG. 16

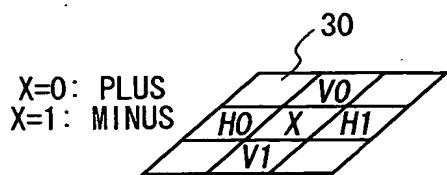


FIG. 17A

		VALUE
{V0, V1}	[SIGNIFICANT OF PLUS, SIGNIFICANT OF PLUS]	1
	[SIGNIFICANT OF PLUS, INSIGNIFICANT]	
{H0, H1}	[SIGNIFICANT OF PLUS, SIGNIFICANT OF MINUS]	0
	[INSIGNIFICANT, INSIGNIFICANT]	
{V0, V1}	[SIGNIFICANT OF MINUS, SIGNIFICANT OF MINUS]	-1
	[SIGNIFICANT OF MINUS, INSIGNIFICANT]	

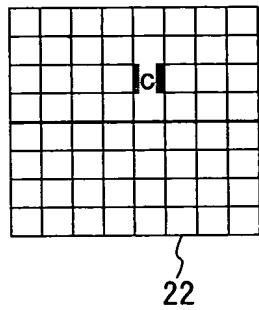
FIG. 17B

BIT=X xor XR

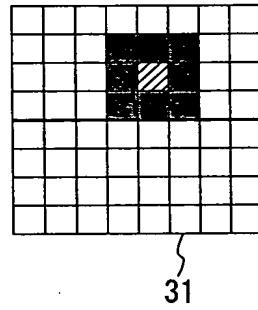
H值	V值	CX	XR
1	1	13	0
1	0	12	
1	-1	11	
0	1	10	
0	0	9	
0	-1	10	1
-1	1	11	
-1	0	12	
-1	-1	13	

FIG. 17C

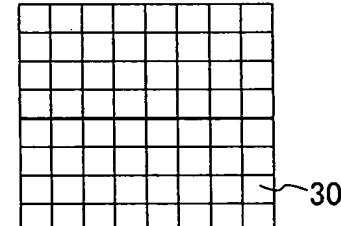
PROCESSING BIT PLANE



SIG PLANE



PLUS AND MINUS SIGNS PLANE



▨ Significance

FIG. 18A

FIG. 18B

FIG. 18C

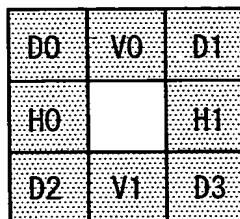
CONDITION	Cx	bit
CHANGE OF SIGNIFICANT DATA TO BIT MODEL CHANGE TO MR PASS FOR THE FIRST TIME WHEN CHANGED TO SIGNIFICANT	Cx	c

FIG. 18D

**FIG. 19A**

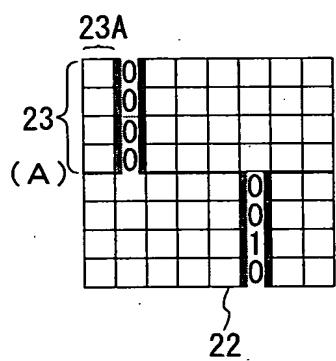
$\Sigma H + \Sigma V + \Sigma D$	PERTINENT COEFFICIENT IS ENCODED WITH THIS PASS FIRST	Cx	BIT
Don't care	No	16	
$>=1$	Yes	15	X
$=0$	Yes	14	

**FIG. 19B**

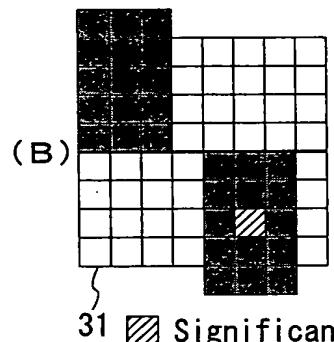


$\Sigma D$  IS THE NUMBER OF SIGNIFICANT ONES IN D0-3

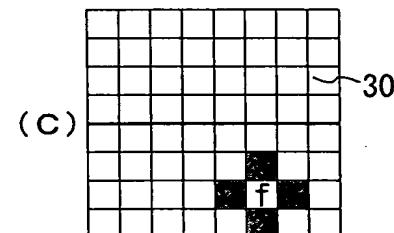
PROCESSING BIT PLANE



SIG PLANE



PLUS AND MINUS SIGNS PLANE



31 Significance

**FIG. 20A**

**FIG. 20B**

**FIG. 20C**

CONDITIONS	Cx	bit
ALL DATA OF STRIPE ARE 0, AND ALL CORRESPONDING VICINITIES ARE NOT SIGNIFICANT	run	c
	run	c
	uniform	c
	uniform	c
AT LEAST ONE OF DATA OF STRIPE IS 1, THERE IS NO SIGNIFICANT IN THE VICINITY	Sign	f
CHANGE TO SIGNIFICANT WITH DATA OF 1	CX	
SAME PROCESSING AS SP PASS IS PERFORMED AFTER THAT	CX	0

**FIG. 20D**

**FIG. 21A**

1	
x	
x	
x	

**FIG. 21B**

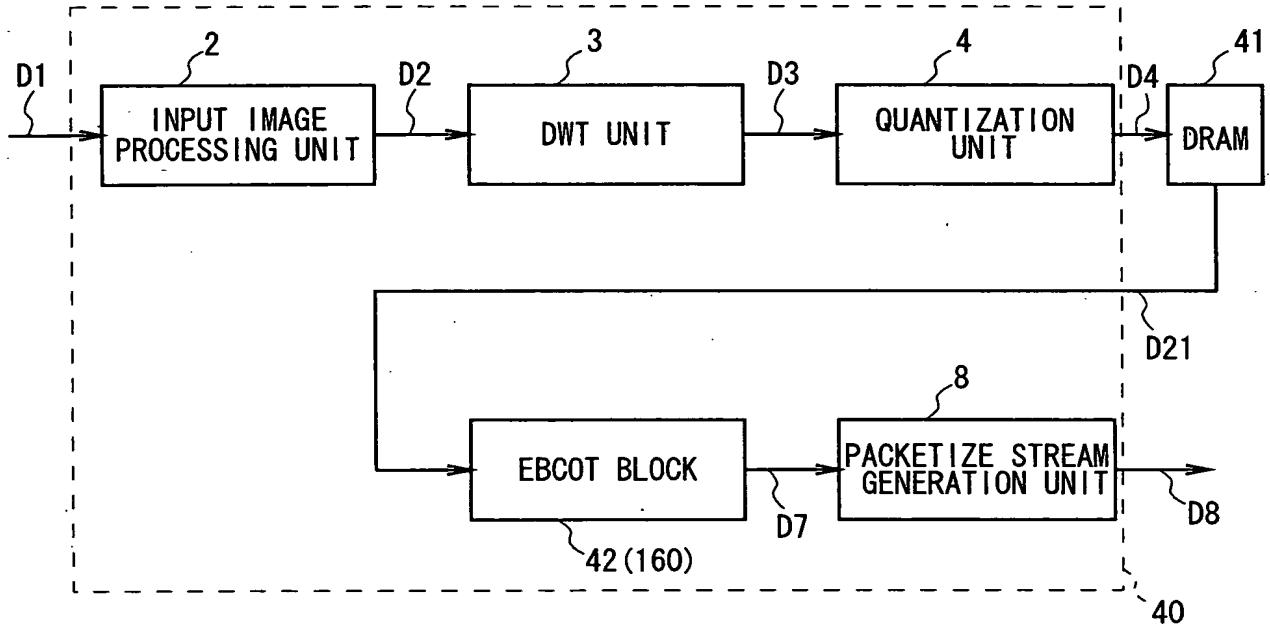
0	
1	
x	
x	

**FIG. 21C**

0	
0	
1	
x	

**FIG. 21D**

0	
0	
0	
1	



**FIG. 22**

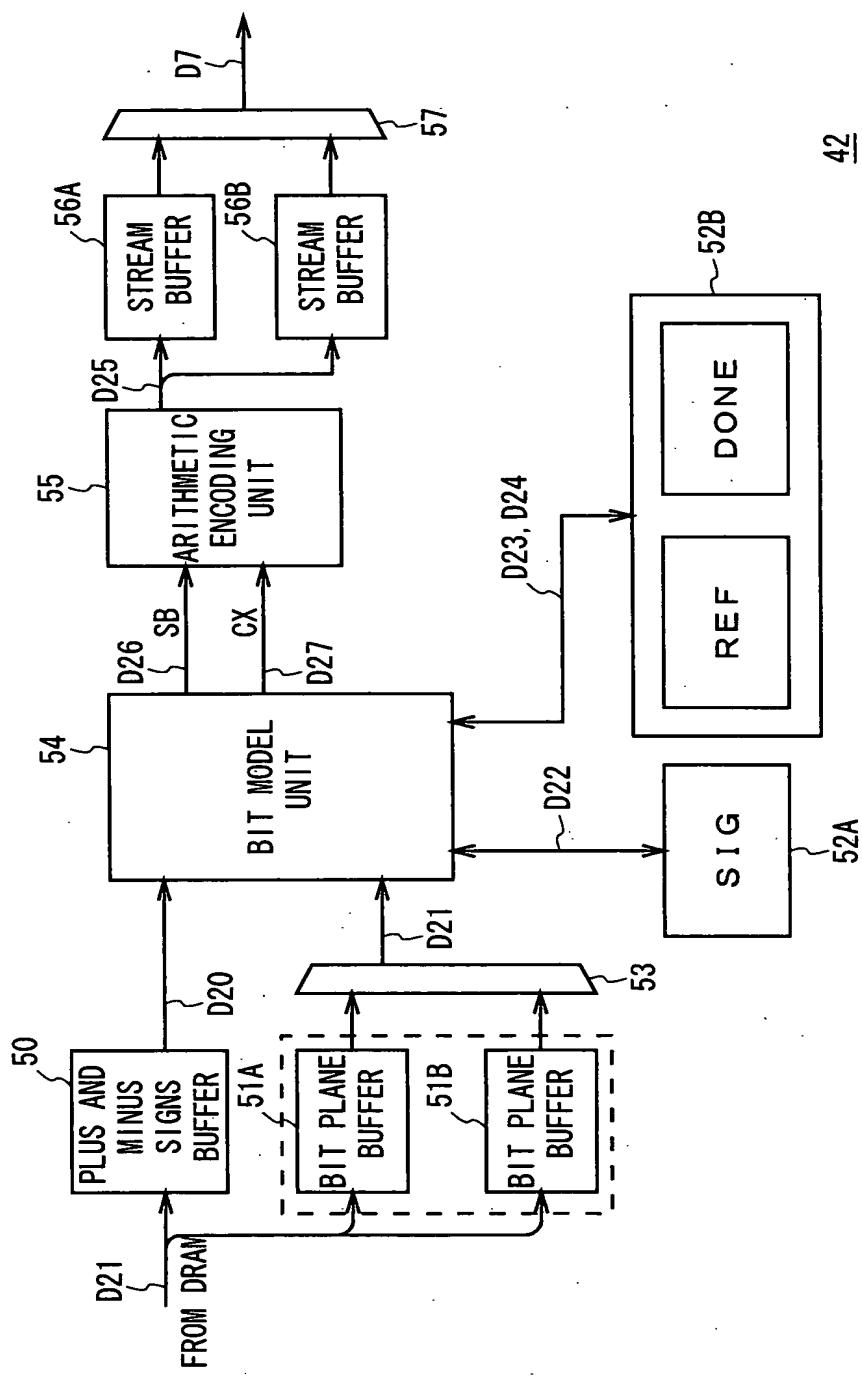


FIG. 23

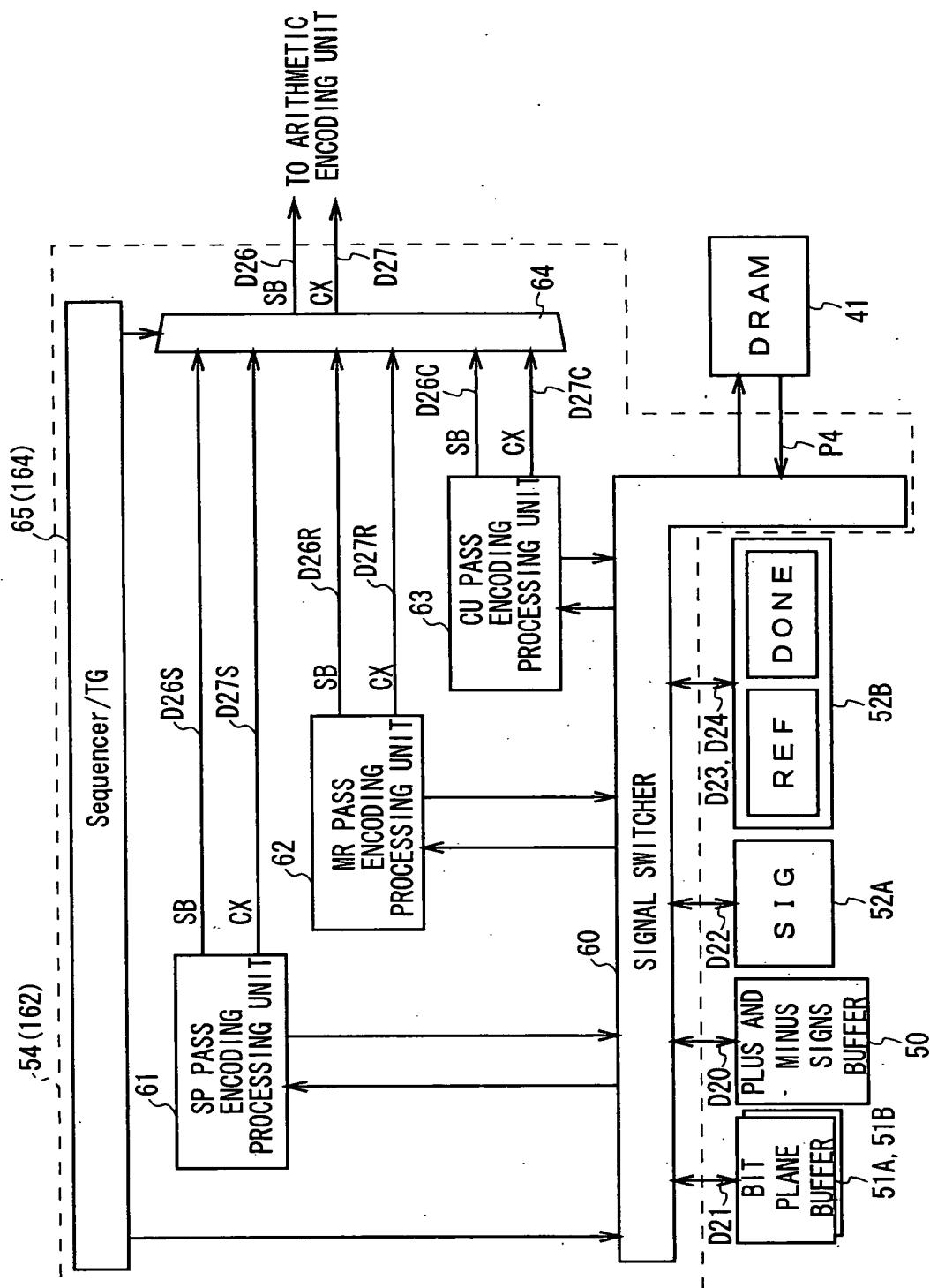


FIG. 24

PROCESSING BIT PLANE

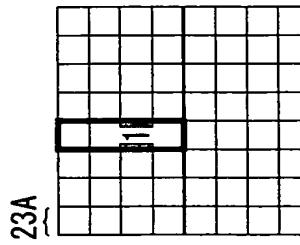


FIG. 25A

SIG PLANE

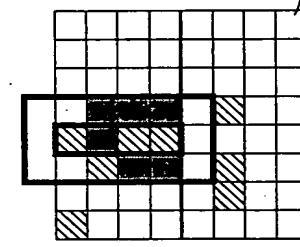


FIG. 25B

PLUS AND MINUS SIGNS PLANE

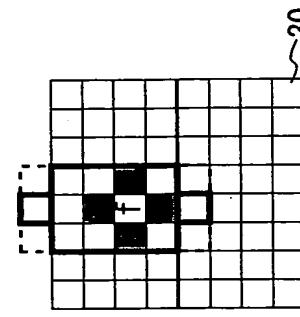


FIG. 25C

DONE PLANE

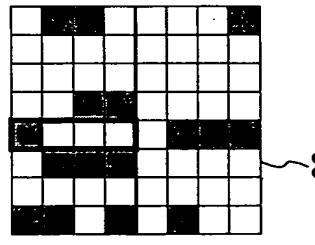


FIG. 25D

REF PLANE

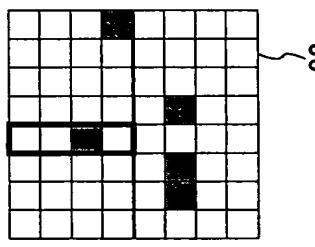


FIG. 25E

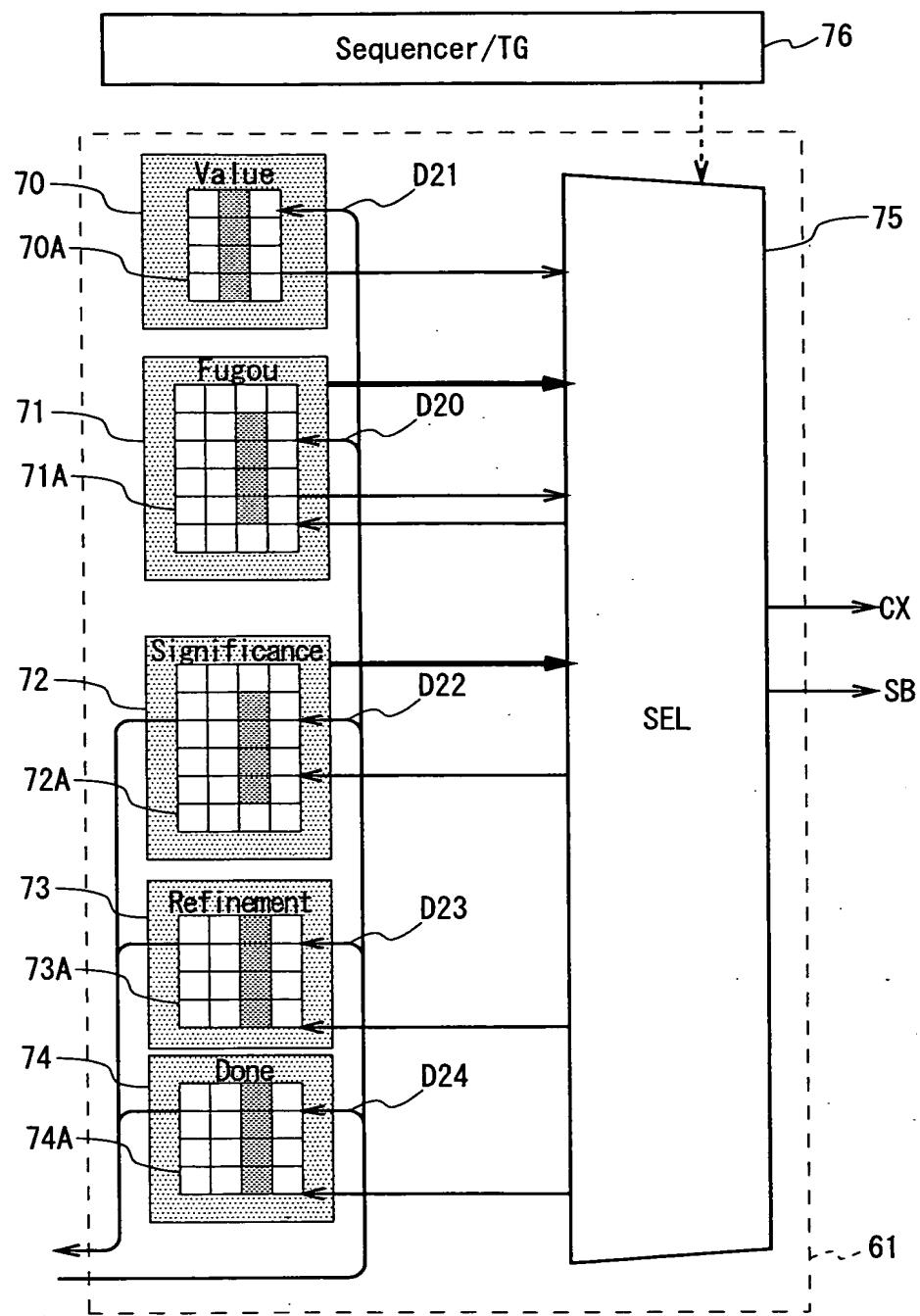


FIG. 26

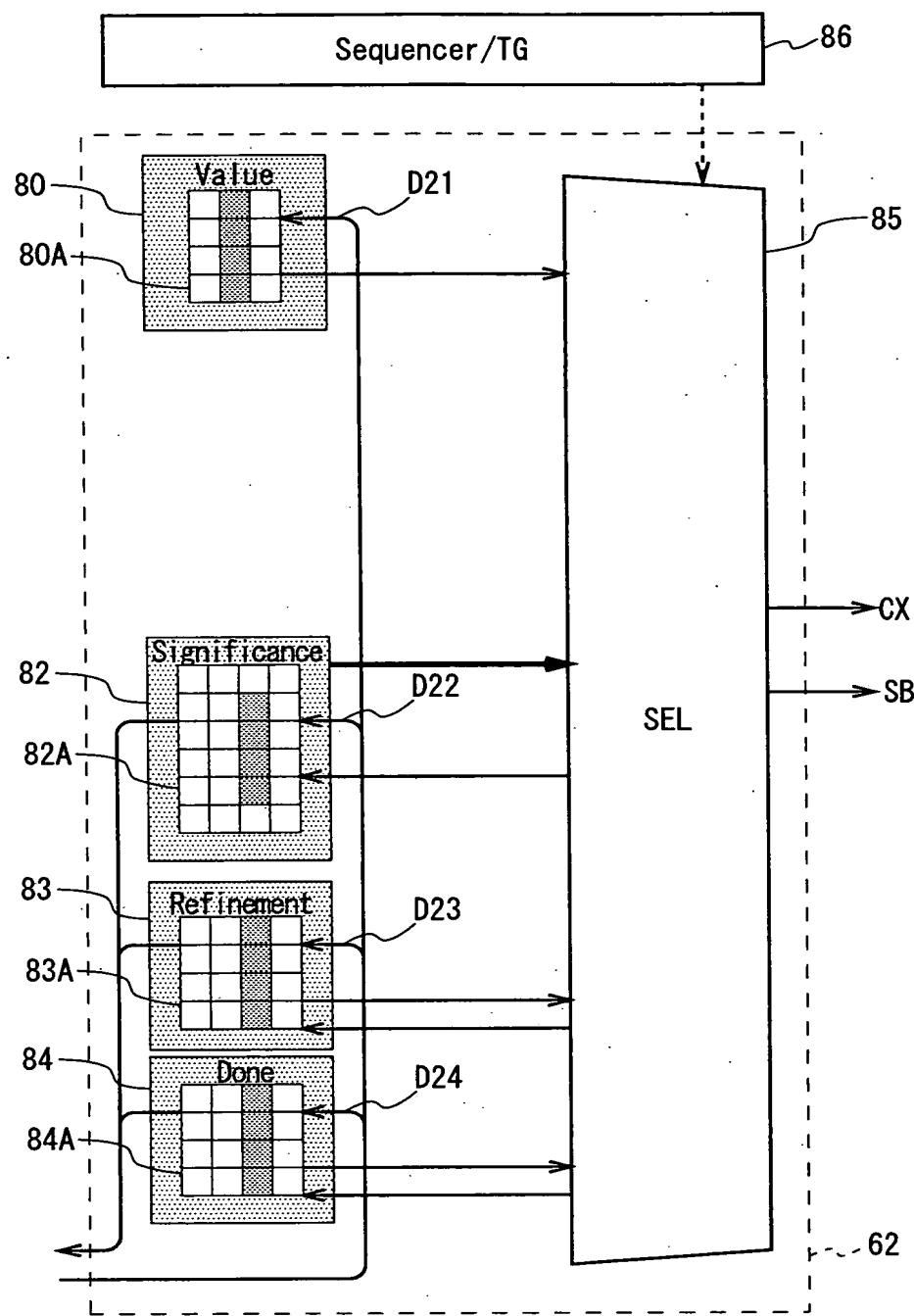
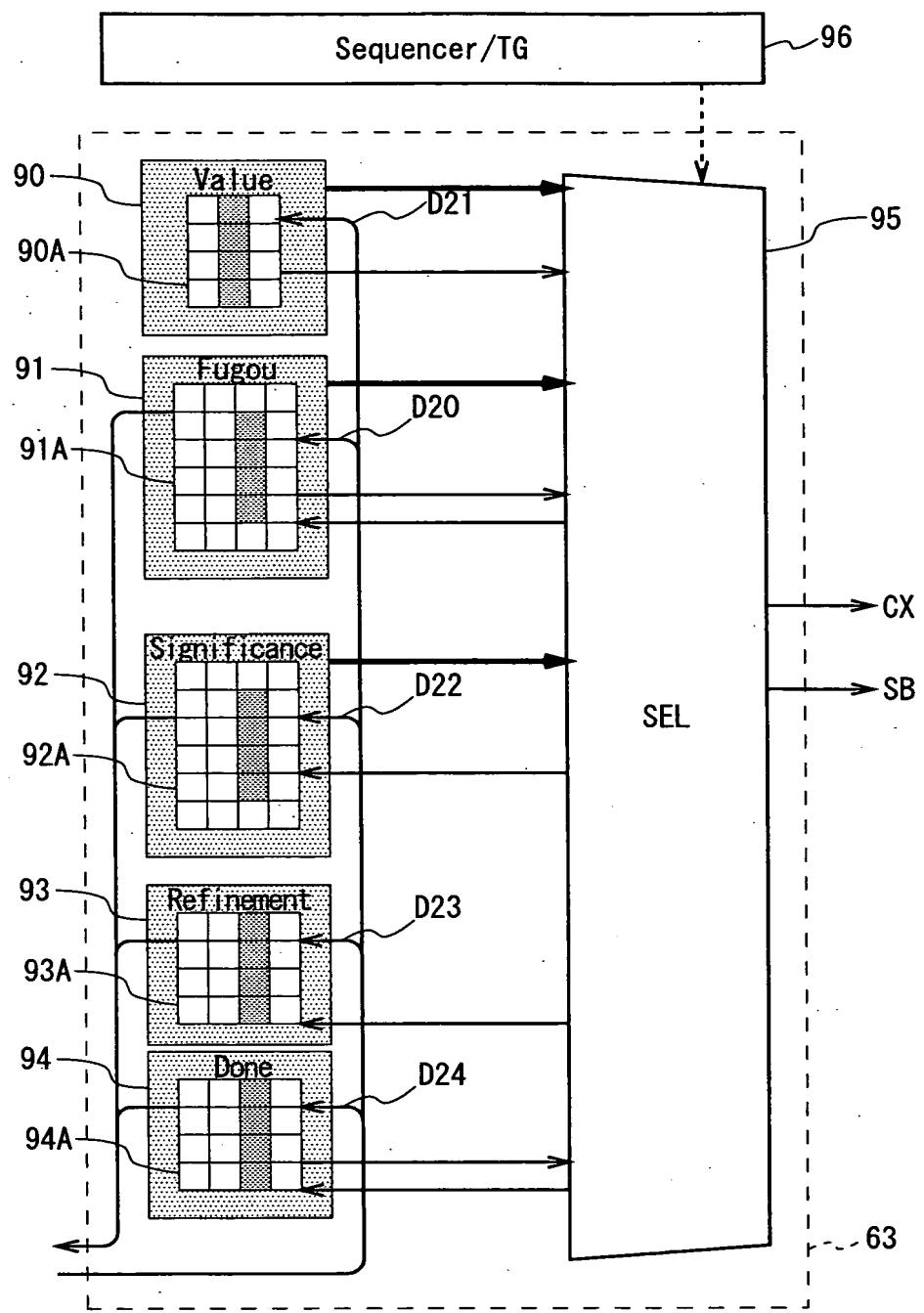


FIG. 27



**FIG. 28**

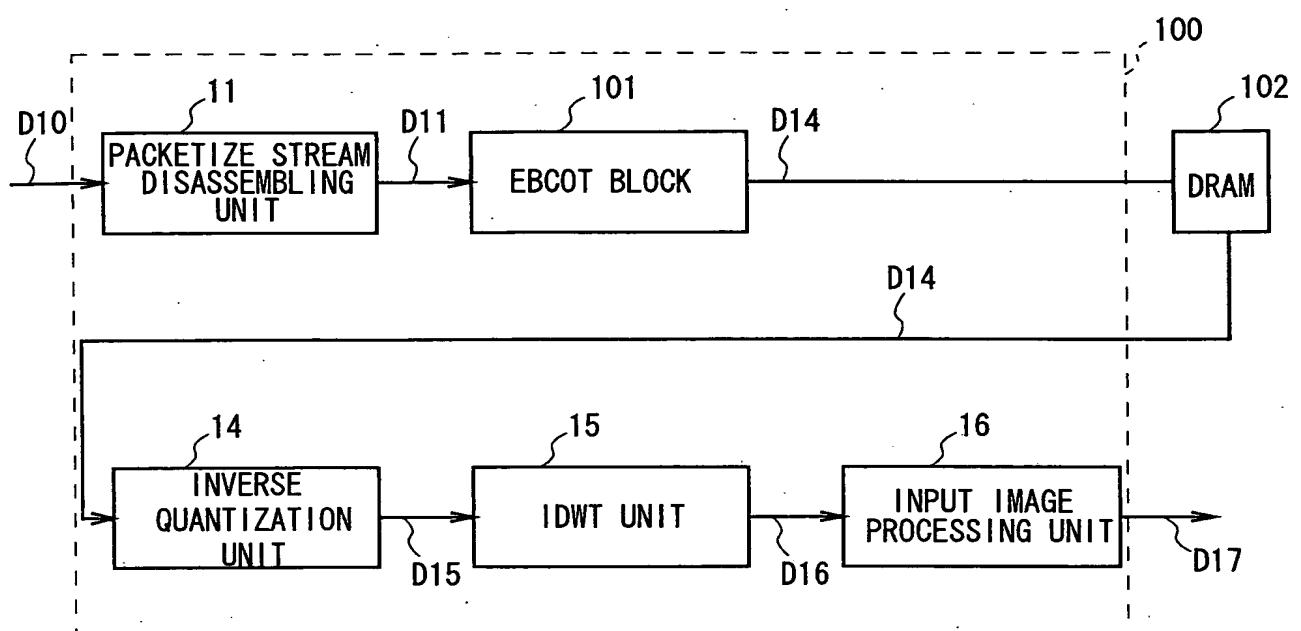


FIG. 29

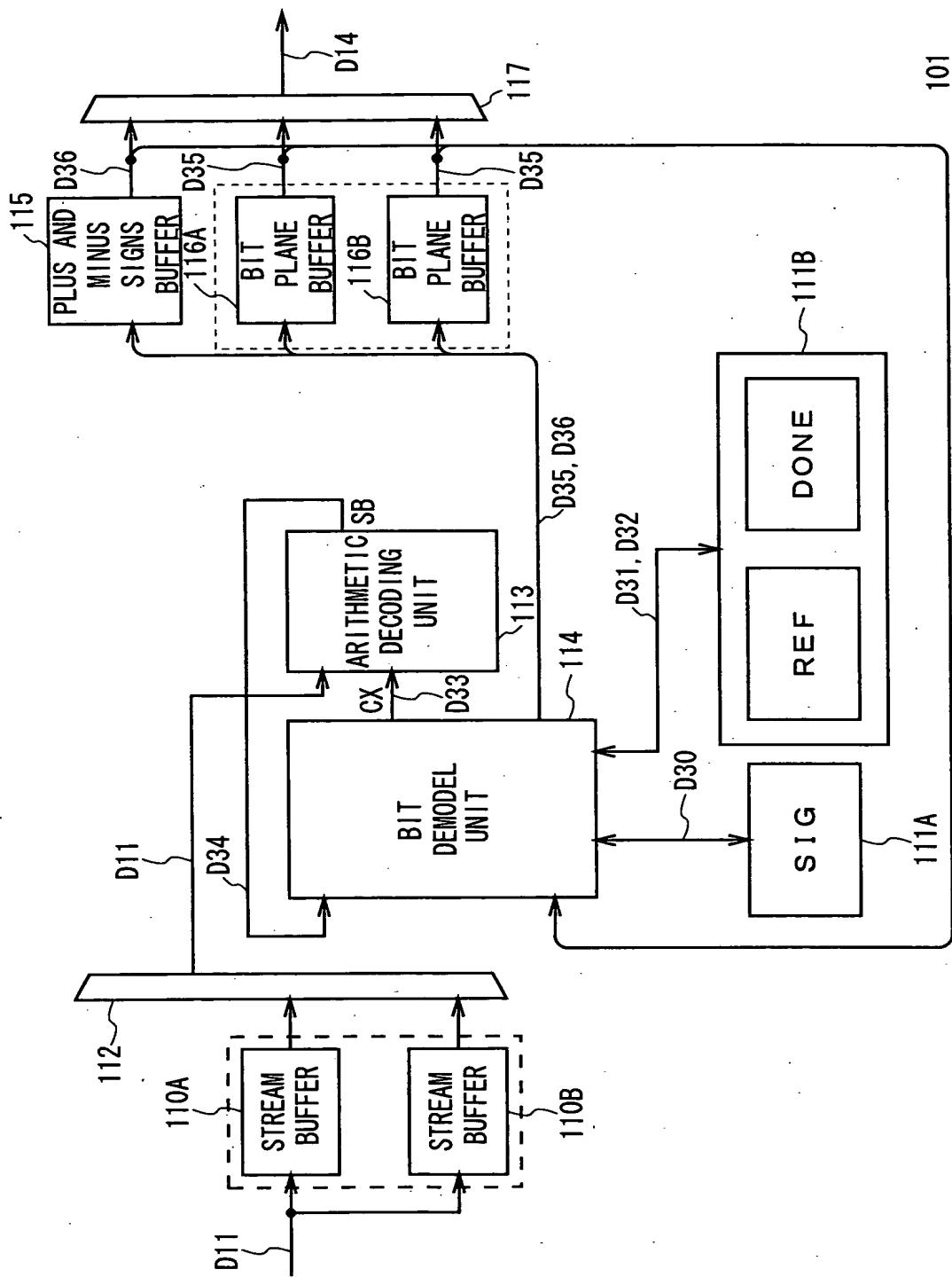


FIG. 30

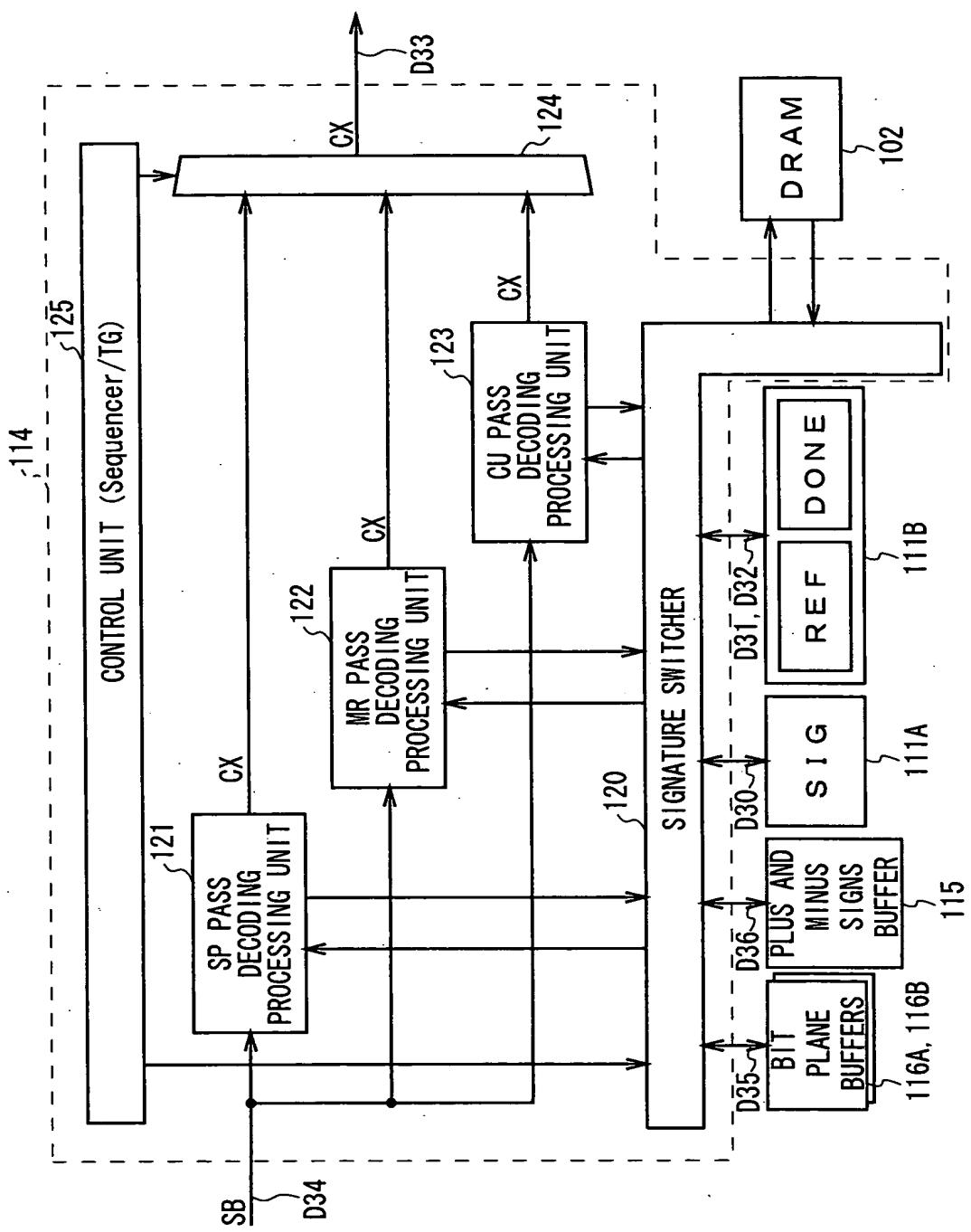


FIG. 31

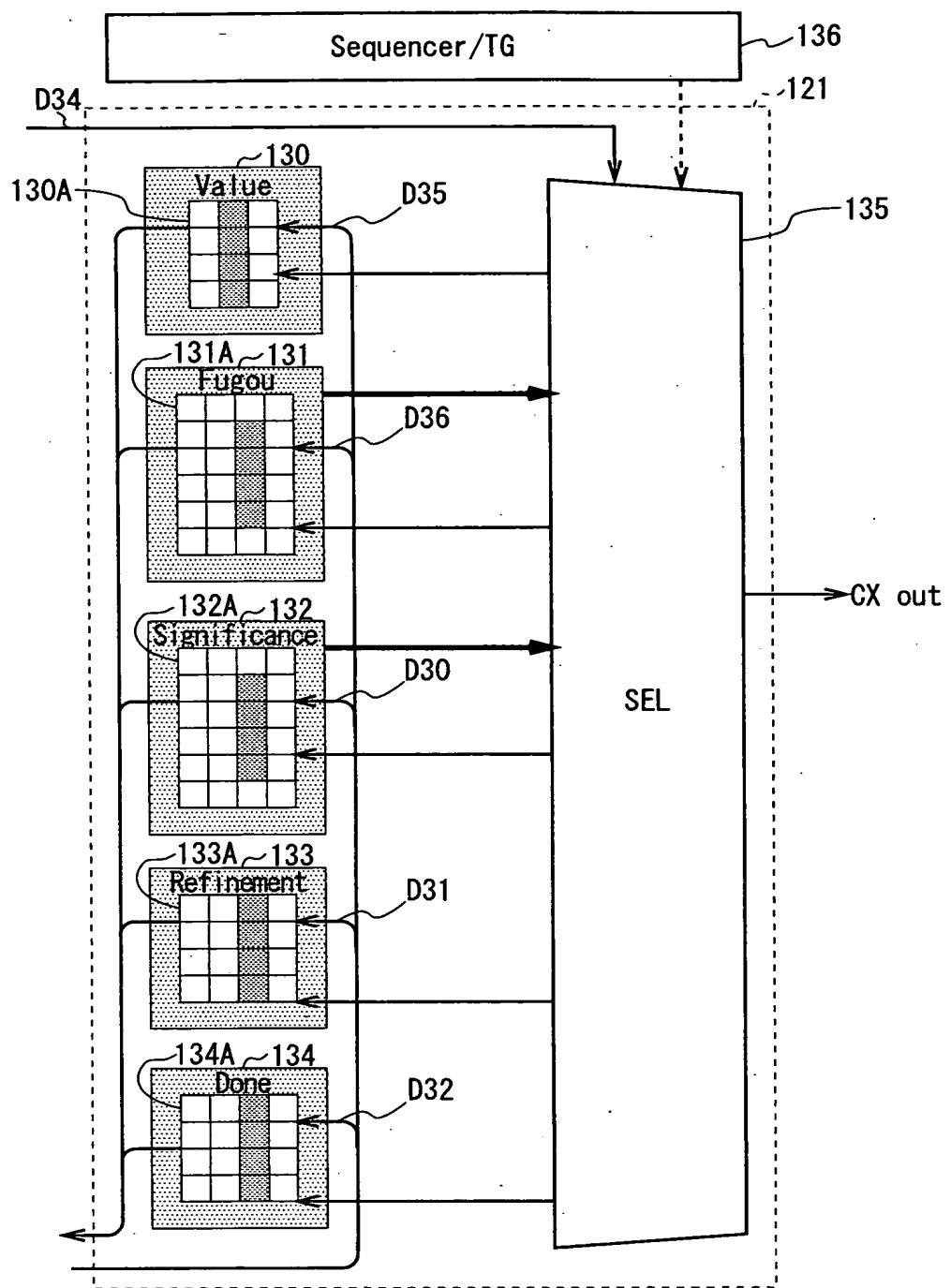


FIG. 32

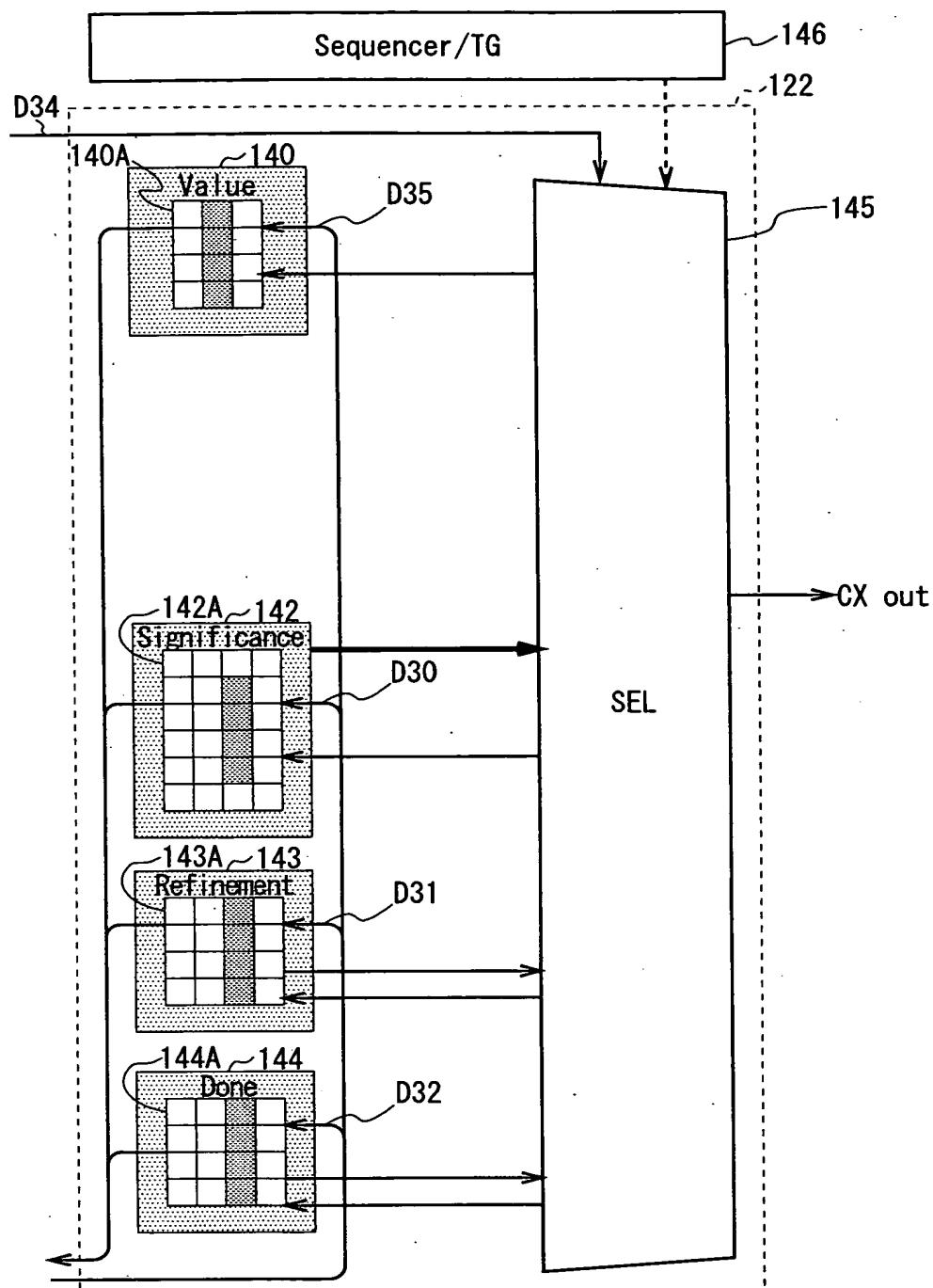


FIG. 33

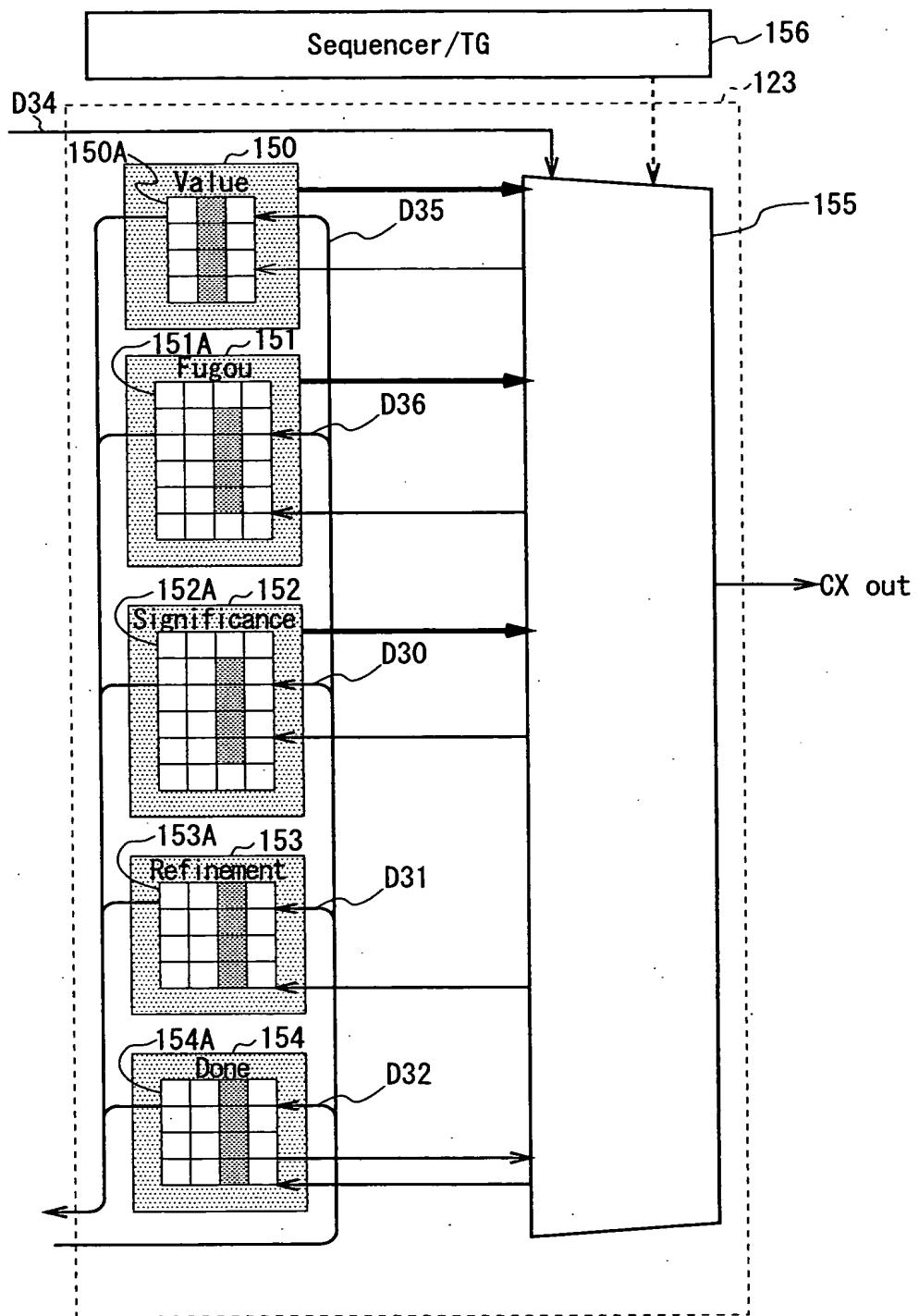


FIG. 34

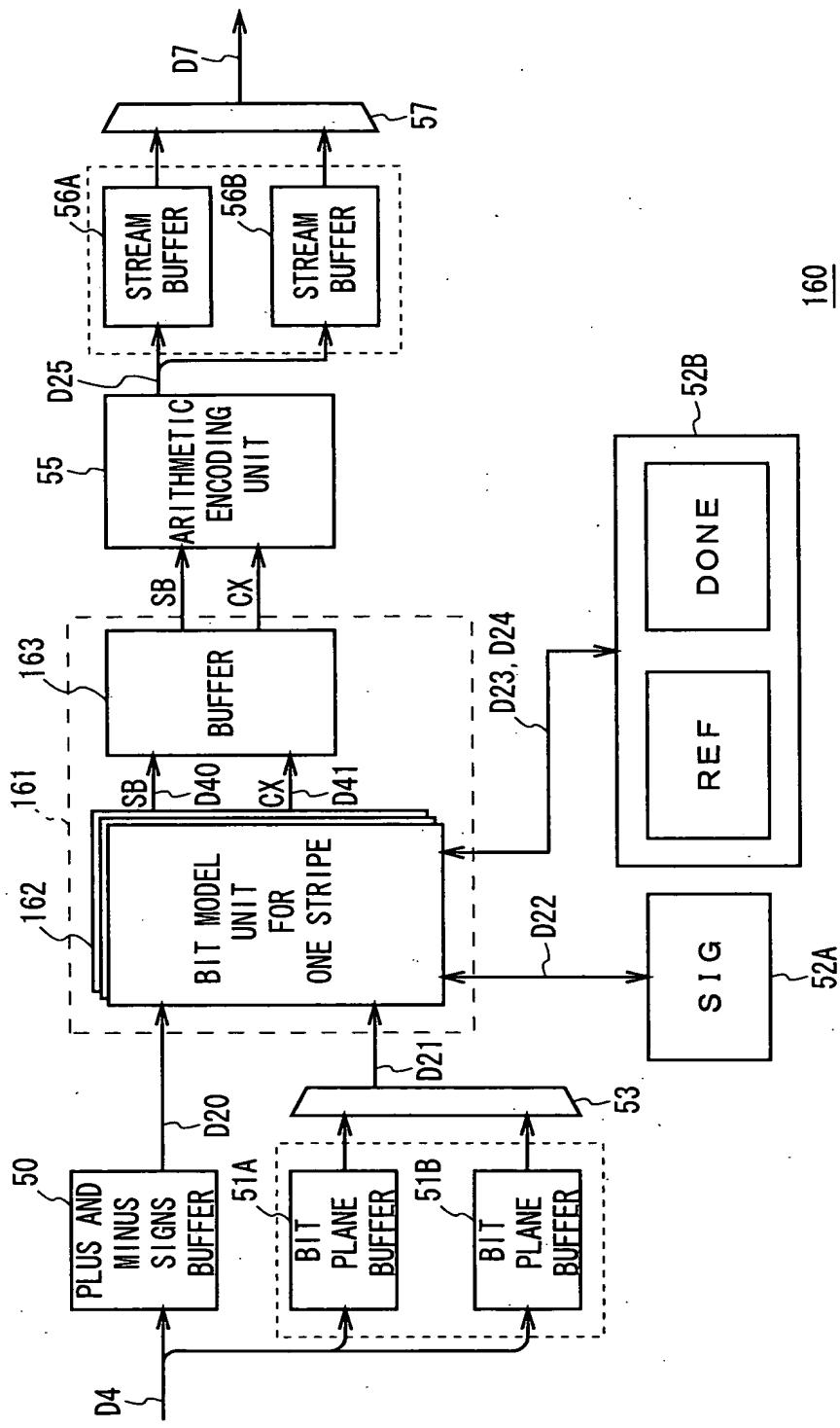


FIG. 35

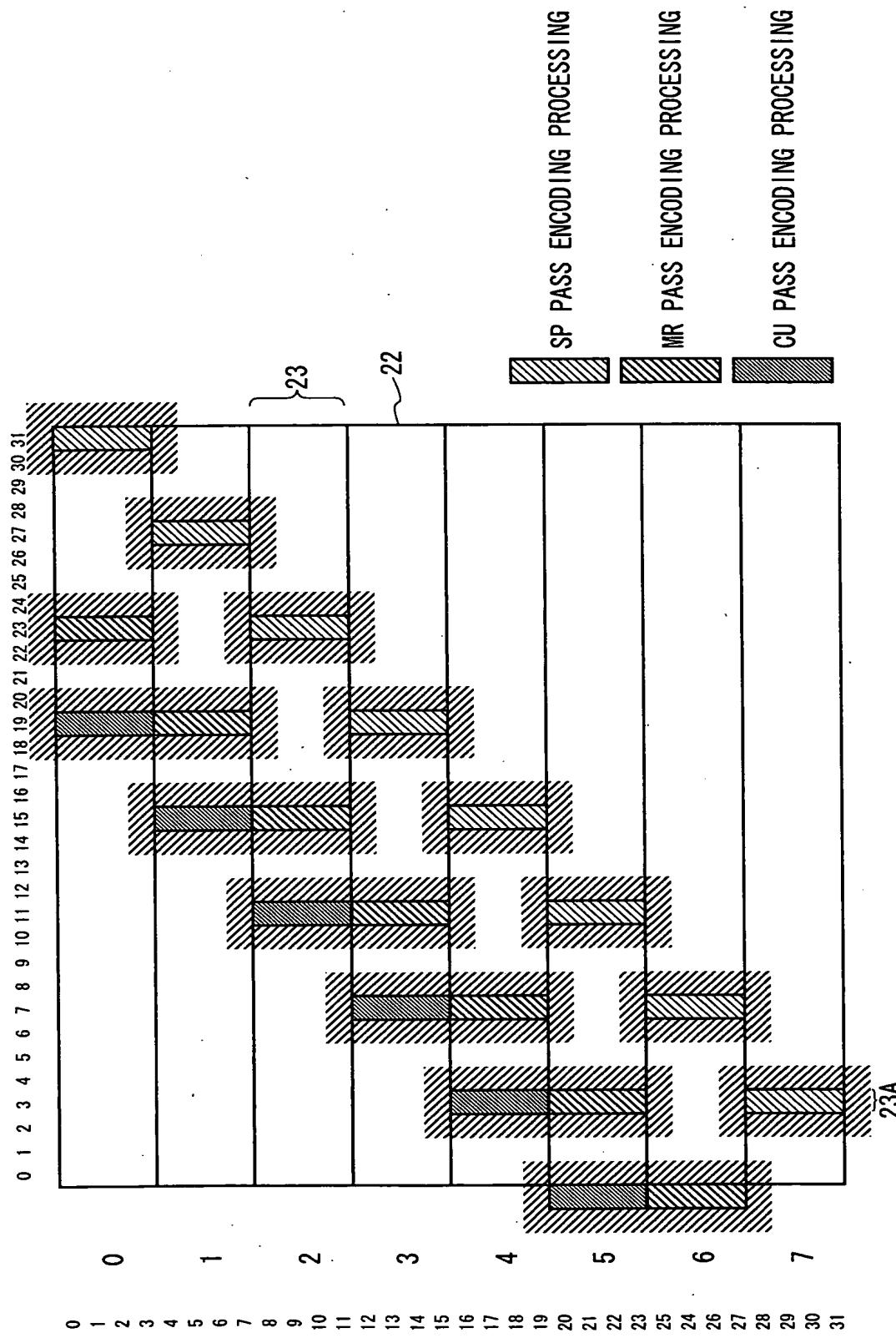


FIG. 36

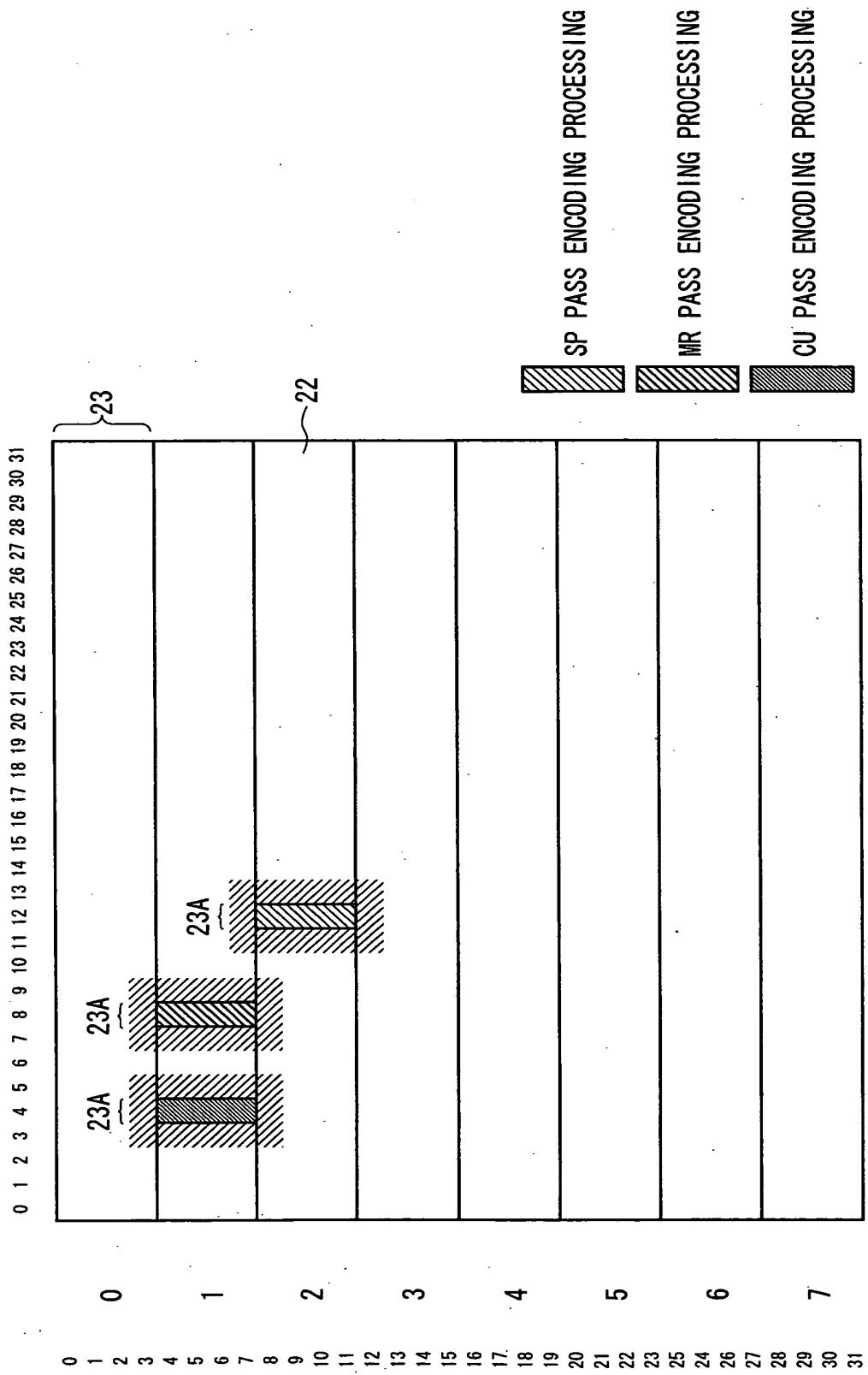


FIG. 37

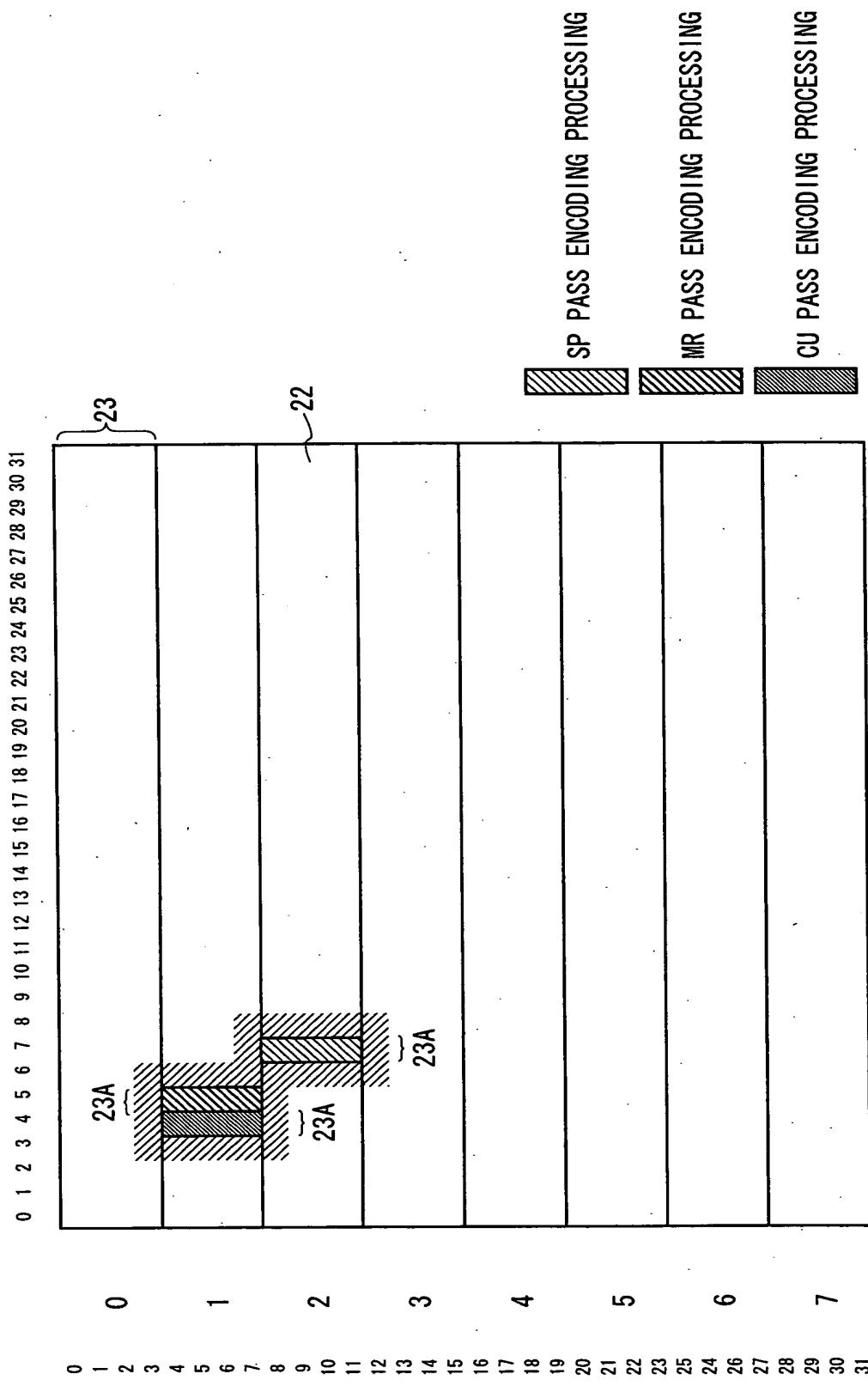


FIG. 38

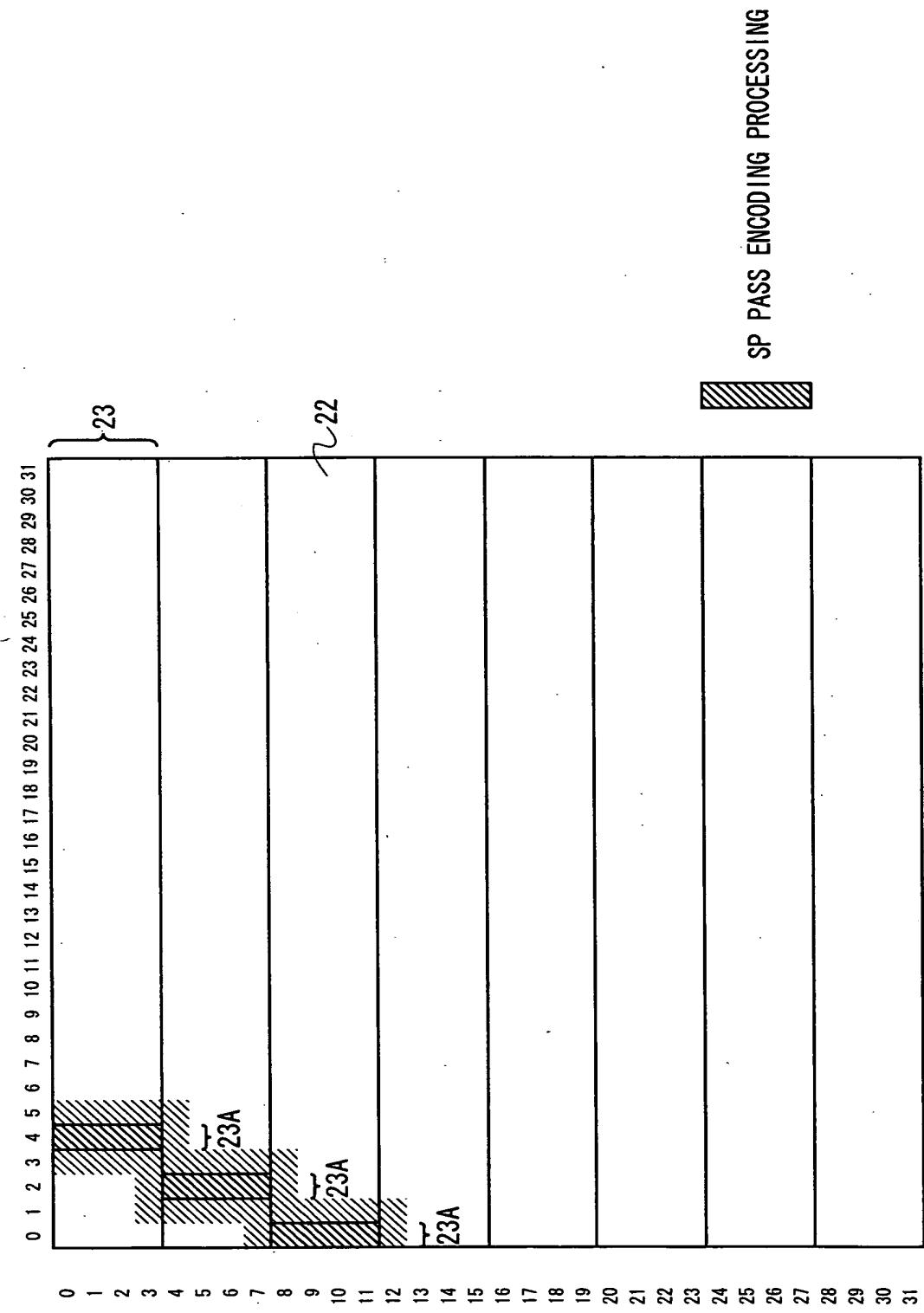


FIG. 39

FIG. 40

